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Applicant, Title of invention, Abstrac	t — e.g. computer semiconduct	or	
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piezoelectric transformer			
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4	BRS	L4	0	l3 and piezoelectric adj1 transformer\$	USPA T	2004/08/3 0 12:38	
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10	BRS	L10	30	piezoelectric adj1 transformer and manufactur\$3 same test\$3	US-P GPUB; FPO; JPO; DERW ENT; IBM_ TDB	2004/08/3 0 13:23	